

# Low Level RF for SRF accelerators.

... based on the European XFEL experience

1. Interfaces to LLRF ?
2. LLRF for large scale accelerators

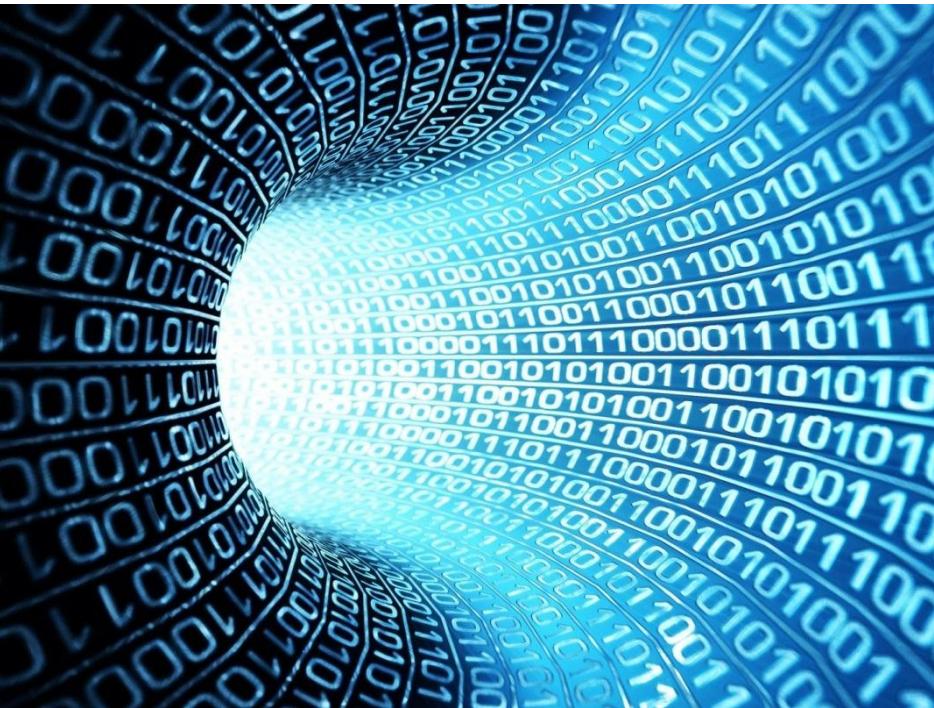
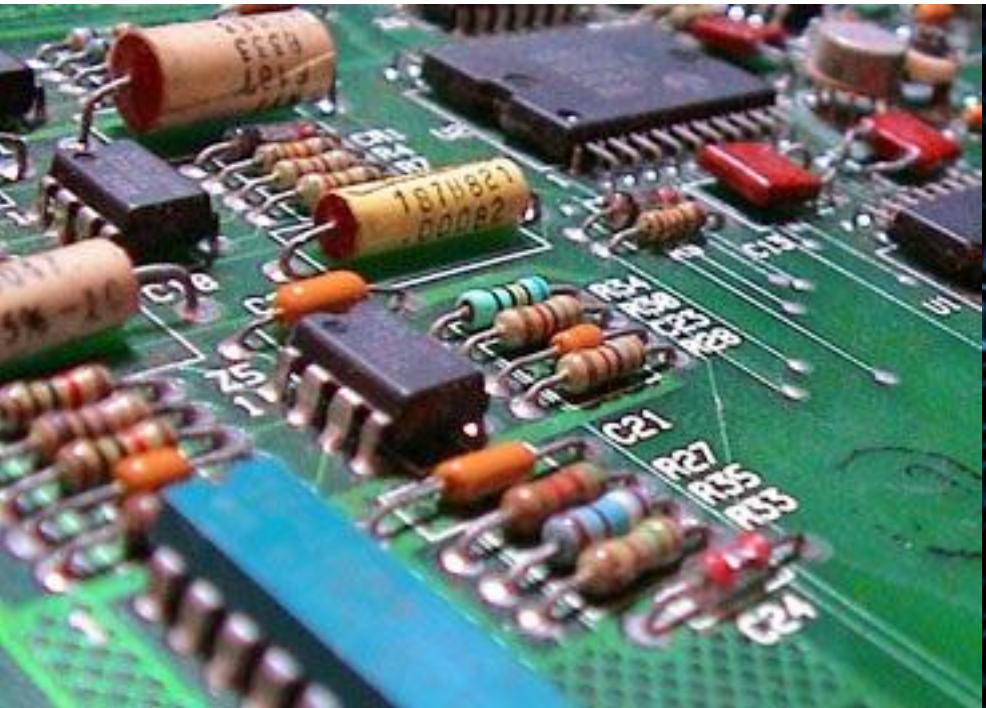
Julien Branlard, DESY  
*for the LLRF team*  
LLRF for SRF accelerators  
Geneva, September 3<sup>rd</sup> 2014

# WHY TALK ABOUT LLRF ?

- “Everything already demonstrated since analog LLRF systems.”

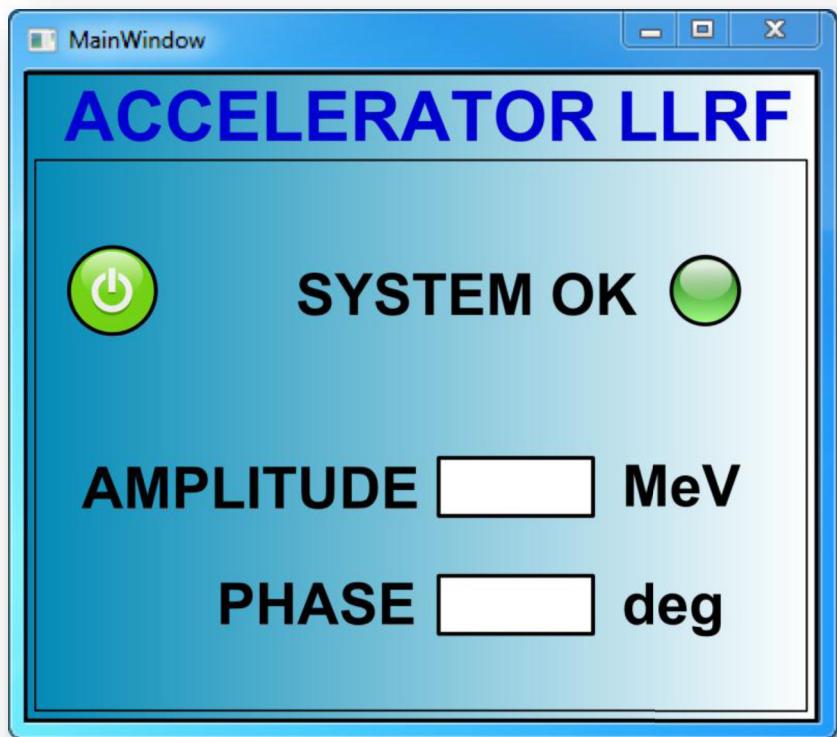
BUT

- “New technologies open new possibilities, offer new challenges.”



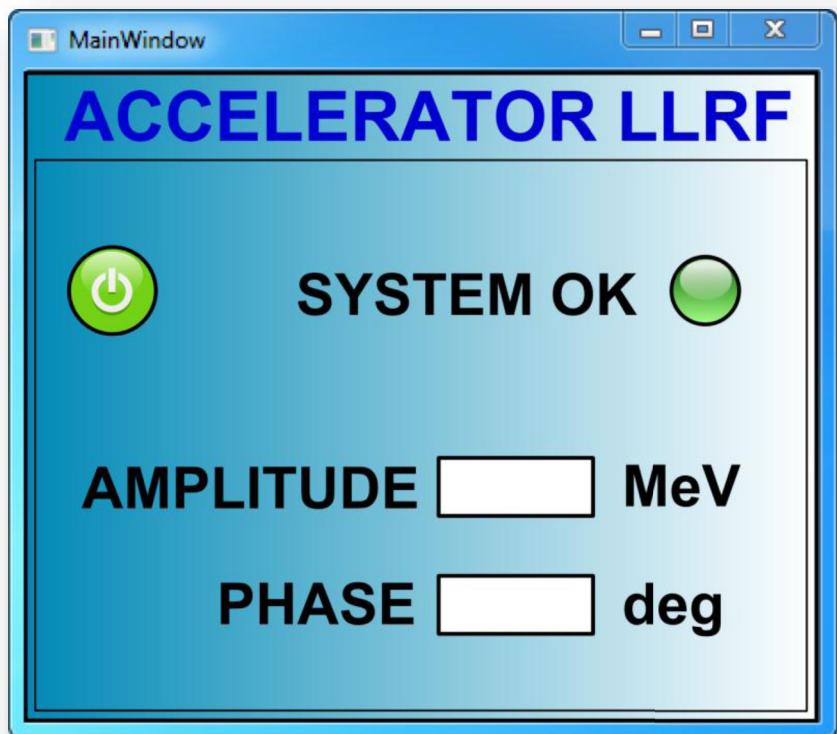
# WHAT IS LLRF ?

- Interface to « The Ultimate LLRF System »

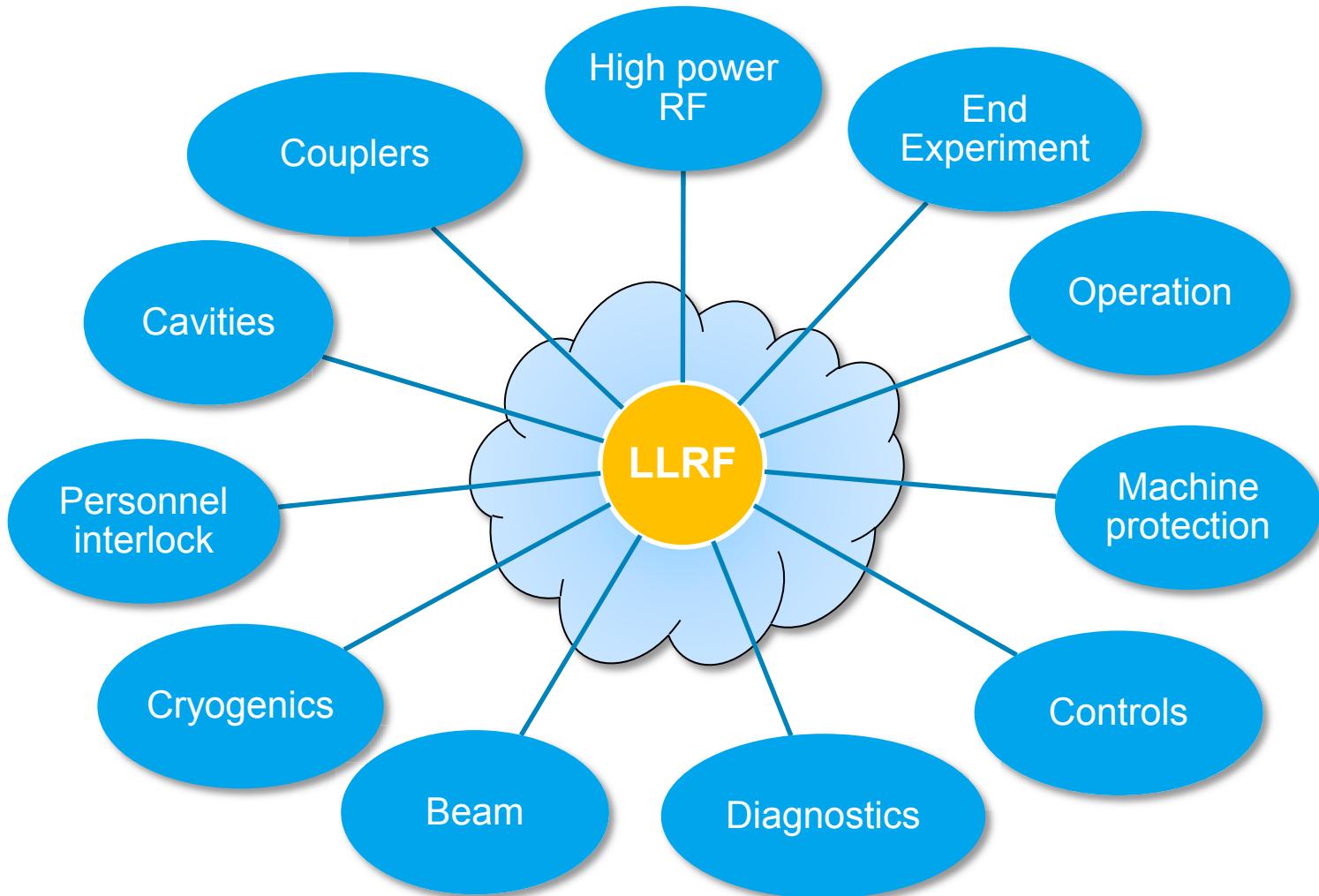


# WHAT IS LLRF ?

- Interface to « The Ultimate LLRF System »

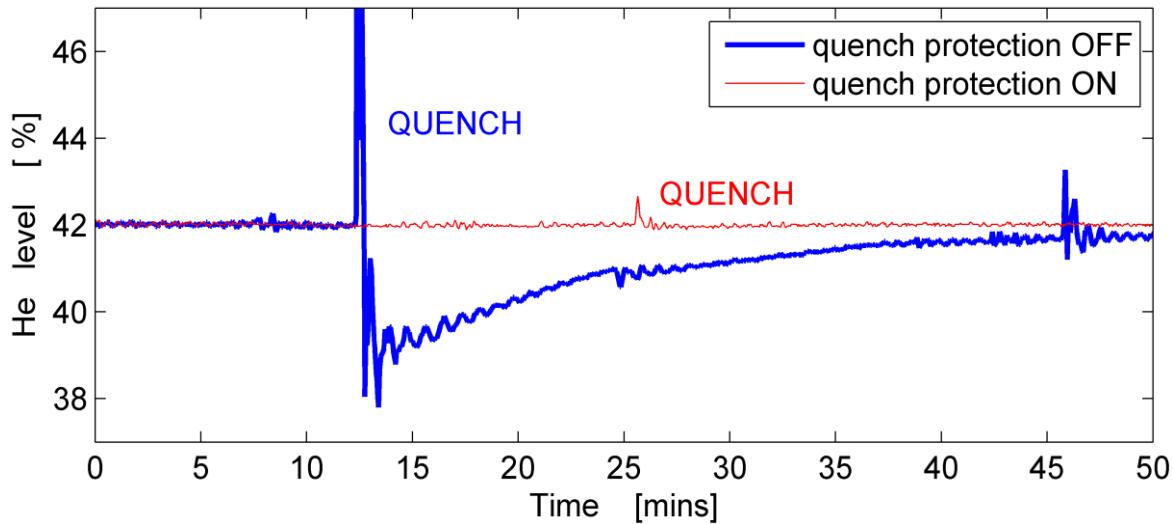
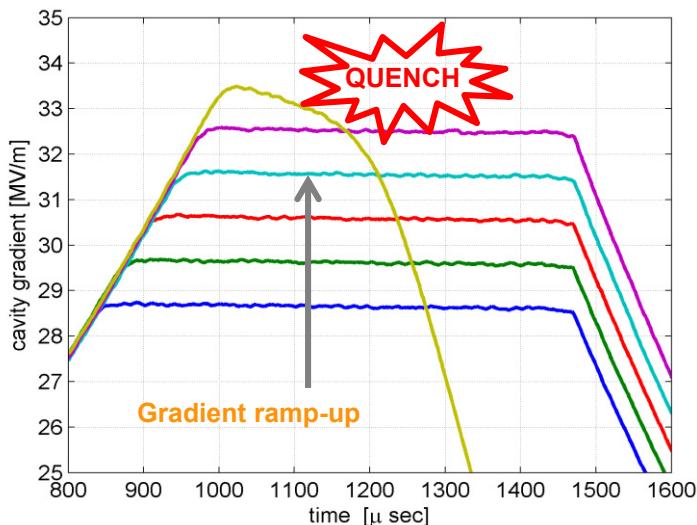
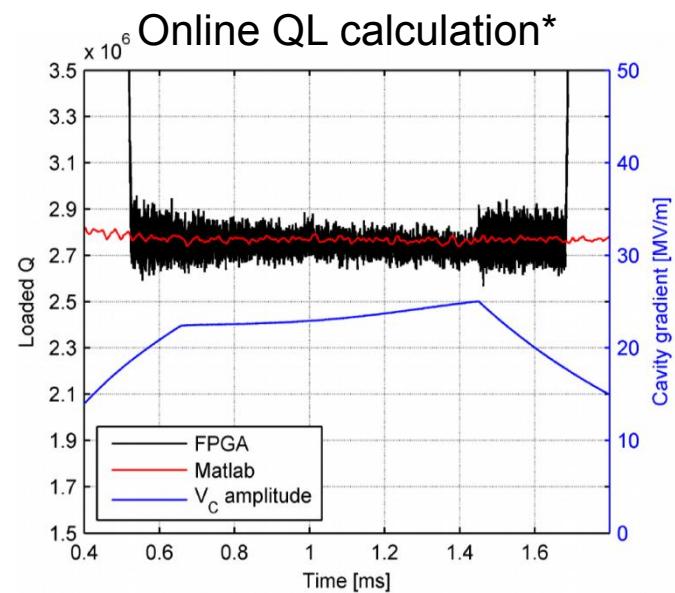


# WHERE DOES LLRF STOP ?



- > INTERFACES TO LLRF
- > LLRF FOR LARGE SCALE ACCELERATORS

- Quench detection
- Heat load anticipation / compensation
- Cryo OK → tuners, piezo



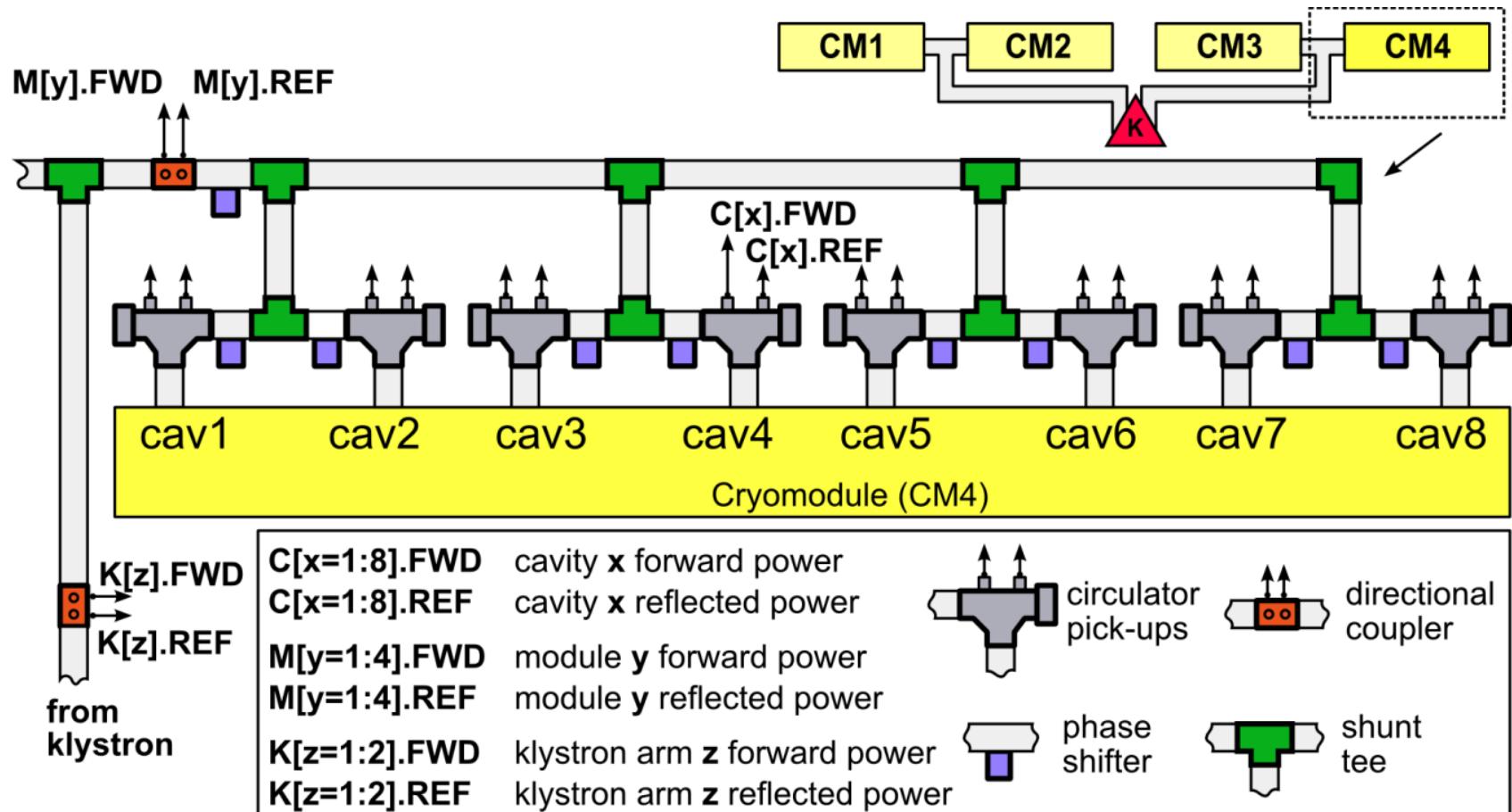
\* Courtesy: R. Rybaniec

"Real-time Estimation of Superconducting Cavities Parameters"

IPAC 2014, Dresden Germany

# LLRF and High Power RF (1/2)

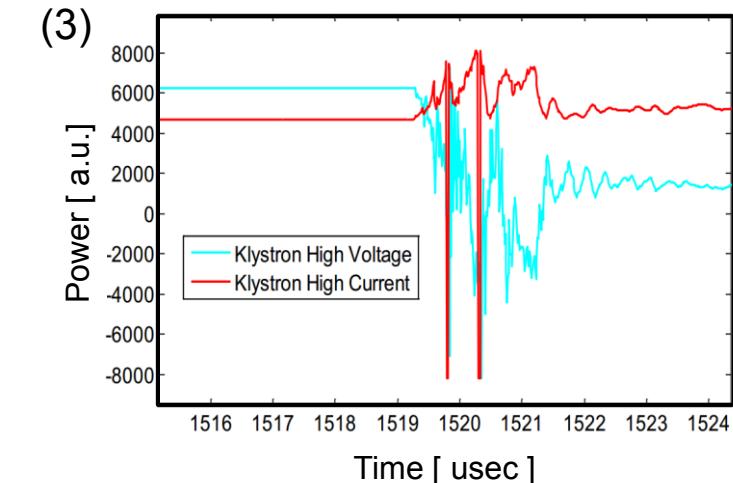
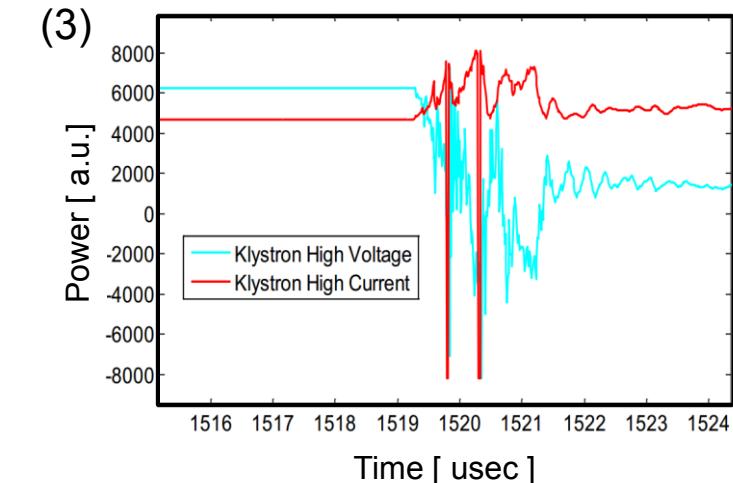
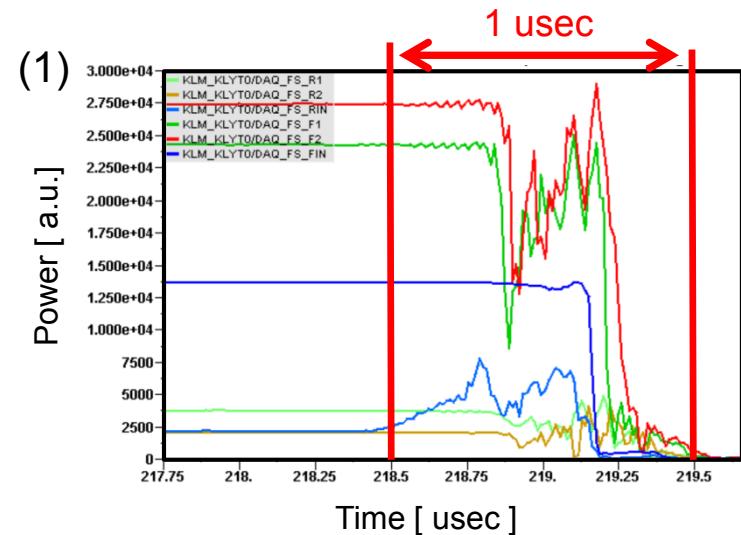
## > Power sub-distribution and phase shifters control



## > Klystron monitoring

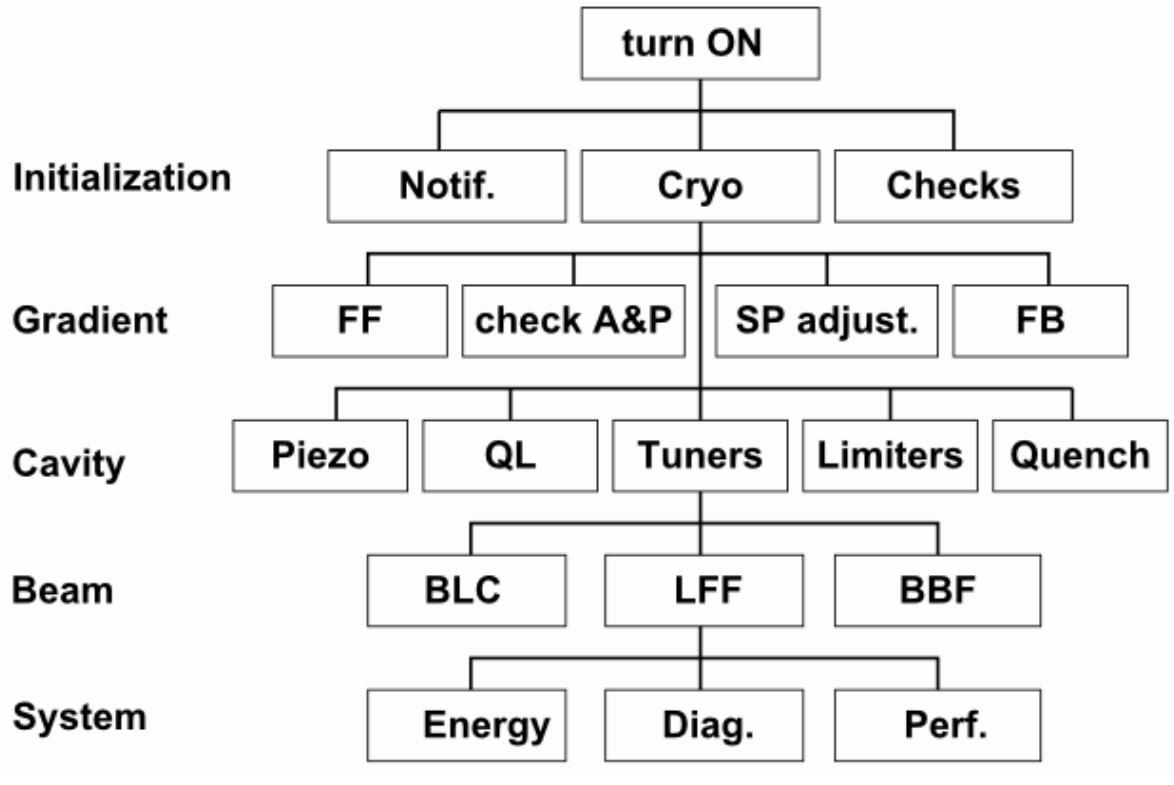
- (1) - RF break-down
- (2) - “Too-high” reflected power
- (3) - High voltage break-down

- Fast interlock of the LRF drive
- 200 nsec reaction time



# LLRF and Operation (1/2)

- RF station ON/OFF
- Finite State Machine
- Exception handling



FSM example

# LLRF and Operation (2/2)

- RF station ON/OFF
- Finite State Machine
- Exception handling
- Operator interface GUI
- Alerts / warning visibility

*Intuitive GUIs*

*Layered complexity*

*Explicit alarms*

*Panel navigation*

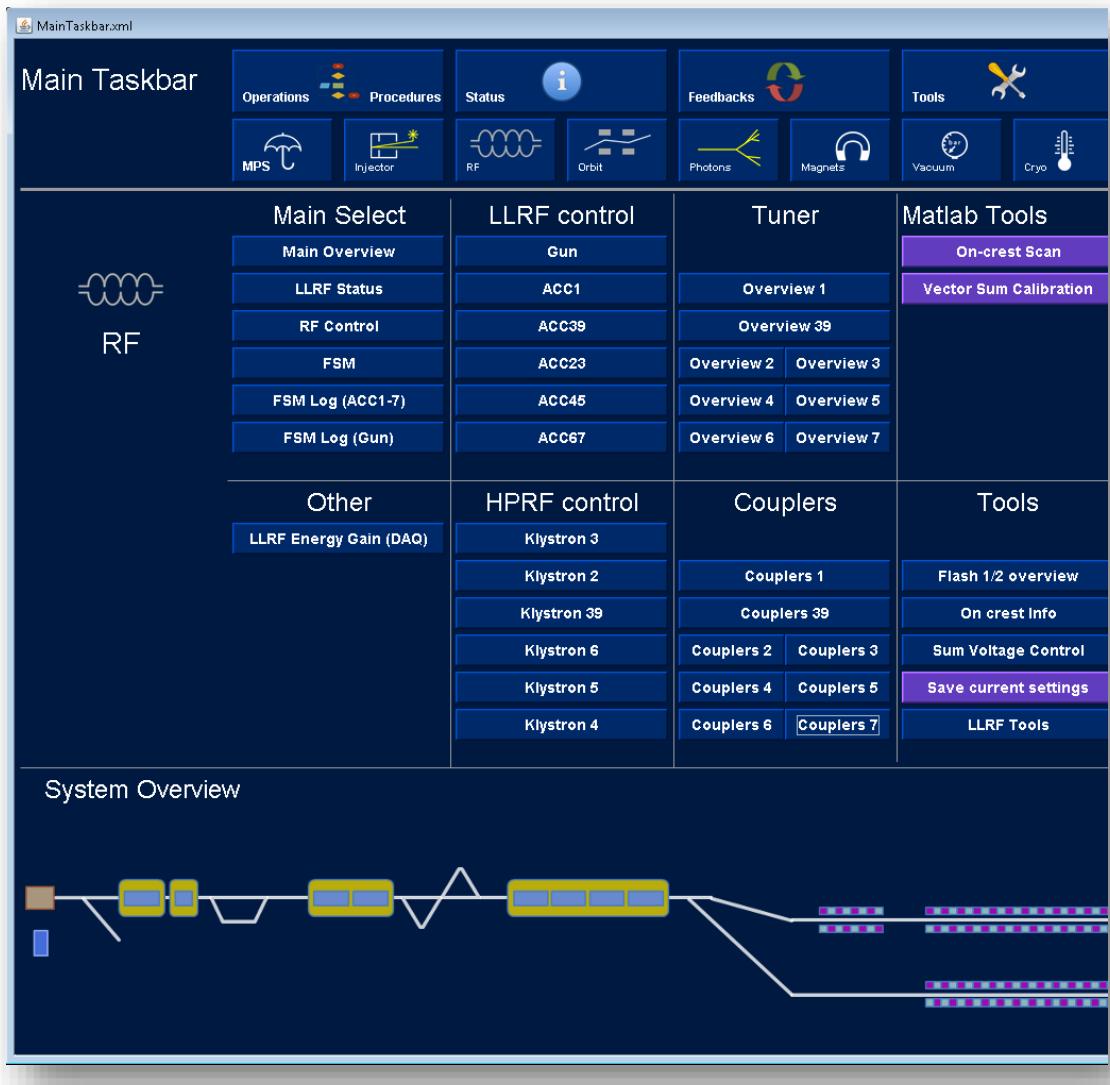
*Complexity abstraction*

*Concentration of relevant data*

*Same data, different representation*

...

- **Automation**



## > Tuner motor

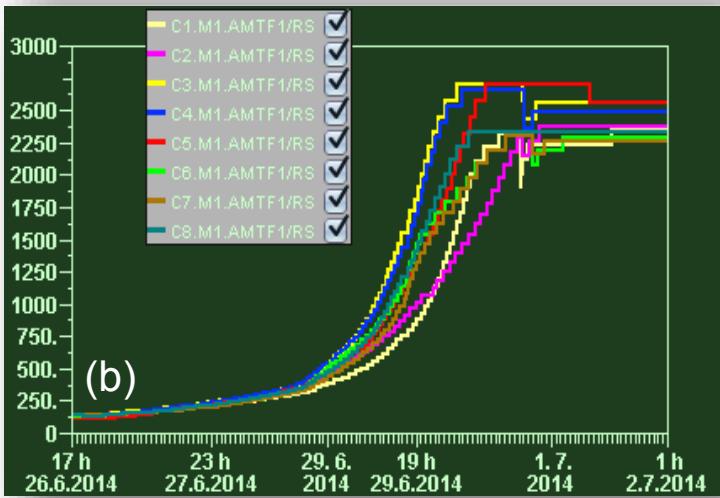
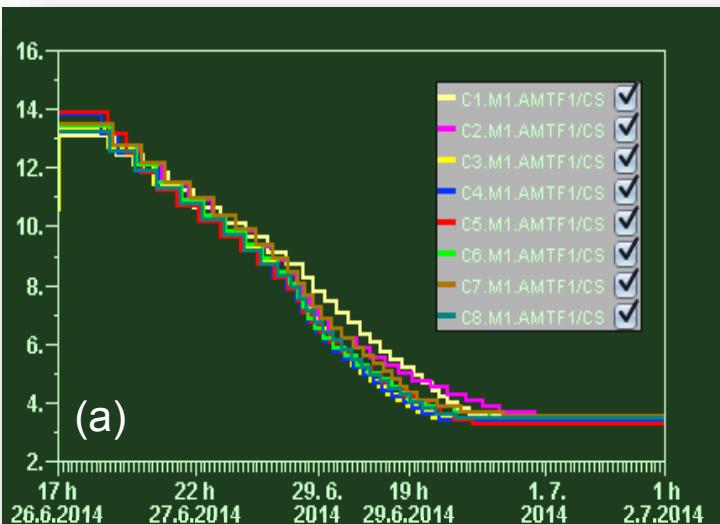
- Tuning
- Detuning
- Cool down / warm up
- Piezo relaxation

## > Piezo

- Microphonics
- LFD compensation
- Cavity fine tuning
- Piezo capacitance measurement

## > Gun

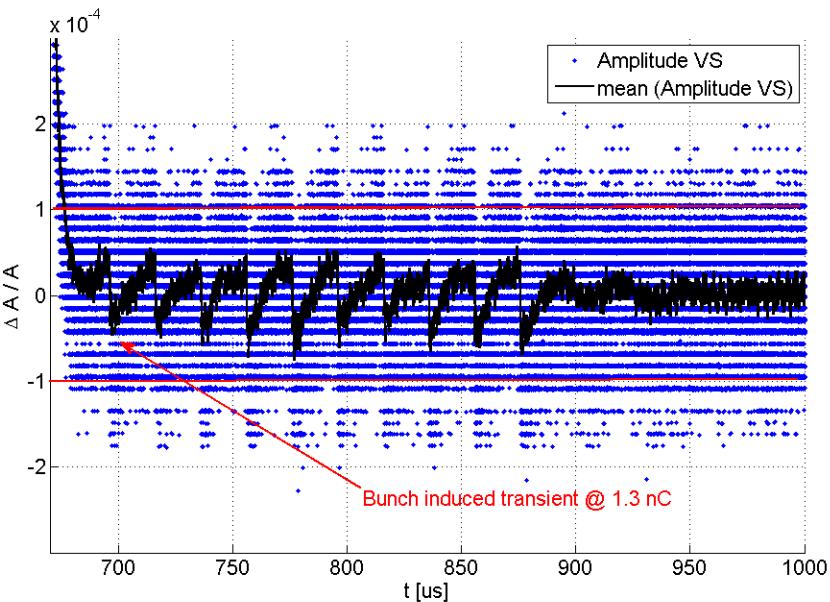
- Cooling (water temperature)
- Flat-top length regulation



Piezo capacitance (a) and resistance (b)  
during cool down

# LLRF and Beam (1/2)

- BAM, BCM → BBF
- Toroid → BLC
- Beam phase
- Beam transients → channel alignment

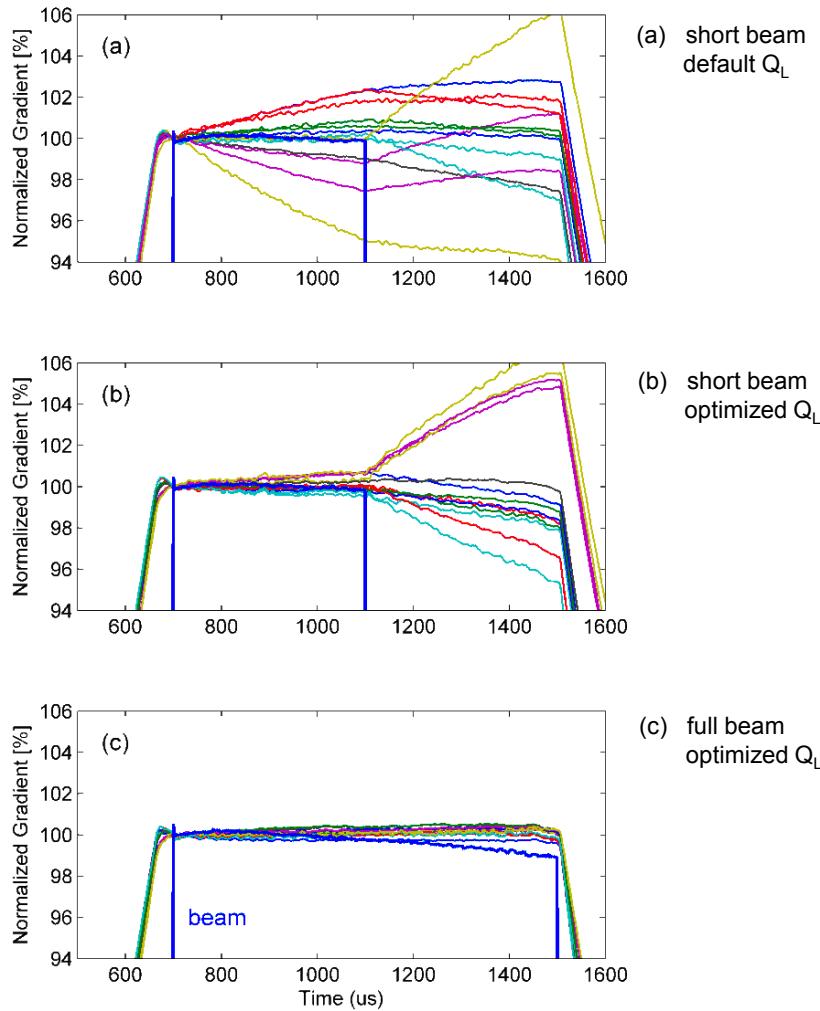


Channel delay alignment using single bunch transients

# LLRF and Beam (2/2)

- BAM, BCM → BBF
- Toroid → BLC
- Beam phase
- Beam transients → channel alignment
- Beam profile → TDS, BC
- Beam energy → VS calibration
- Beam loading →  $Q_L$  adjustments
- ...

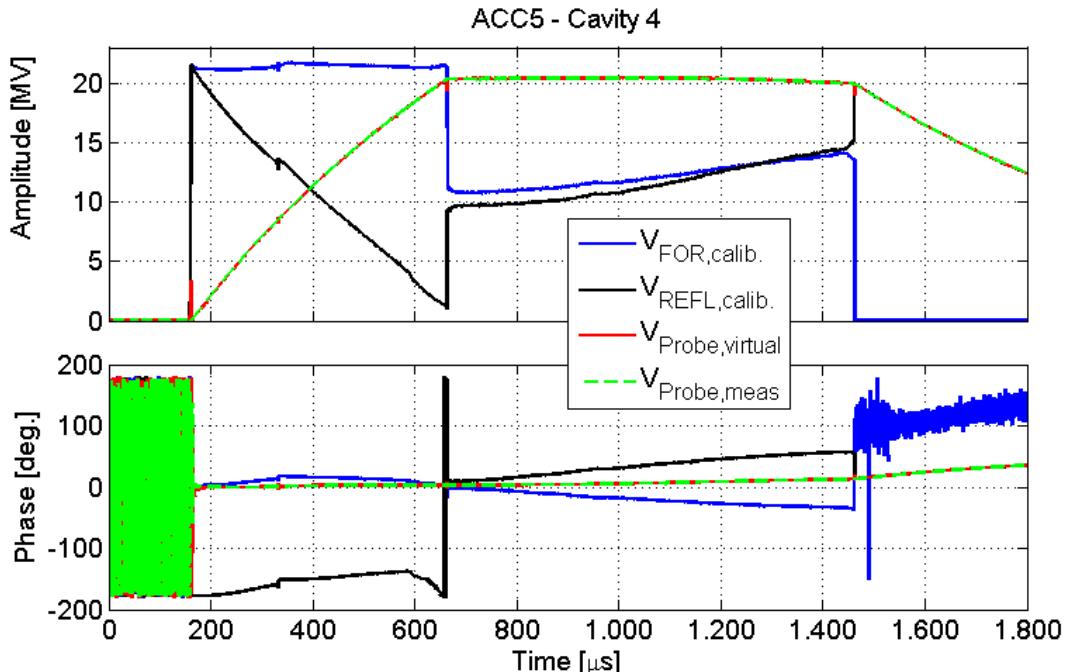
Reference: J. Branlard *et al.*  
"LLRF Automation for the 9mA ILC Tests at FLASH"  
LINAC 2012, Tel Aviv, Israel



# LLRF and Diagnostics (1/2)

- Beam diagnostics (BPM, BLM, BAM, toroid, etc..)
- LLRF diagnostics

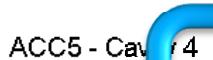
- Performance (intra- inter-train)
- Heat load estimation
- Virtual probe



Virtual probe calculation

# LLRF and Diagnostics (2/2)

- Beam diagnostics (BPM, BLM, BAM, toroid, etc..)
  - LLRF diagnostics



- Performance (intra- inter-train)
  - Heat load estimat
  - Virtual probe

Modules in selected crate	
CRATE :	Schroff GmbH
AMC12 :	SIS8300L

> HOM

## > Radiation

## > System health

- Temperature
  - Fan speed
  - Piezo
  - CPU load
  - ...

Modules in selected crate: XFEL.CRATE/XFELMCHLLA2S		Crate		Fans		Power Modules		Show Graphical	
								Serial:	
CRATE :	Schroff GmbH	IPMB:0x72 Sensor N:131 Type: Temperature Event: Upper Critical going high		info		info		0000000000000001	
AMC12 :	SIS8300L	Struck Innovative Systeme GmbH	U= 1.5	Temp= 33.0		info	green	077	
AMC11 :	SIS8300L	Struck Innovative Systeme GmbH	U= 1.5	Temp= 33.0		info	green	077	
AMC8 :	SIS8300L	Struck Innovative Systeme GmbH	U= 1.5	Temp= 38.0		info	green	077	
AMC7 :	SIS8300L	Struck Innovative Systeme GmbH	U= 1.5	Temp= 36.0		info	green	077	
AMC10 :	SIS8300L	Struck Innovative Systeme GmbH	U= 1.5	Temp= 35.0		info	green	077	
AMC1 :	AM900/412	Concurrent Technologies	U= 0.8	Temp= 32.0		info	green	M22816/003	
AMC4 :	DAMC-TCK7	DMCS	U= 1.1	Temp= 24.0		info	green	0004A391D99A	
AMC9 :	SIS8300L	Struck Innovative Systeme GmbH	U= 1.5	Temp= 38.0		info	green	077	
COOL_UNIT2 :	Fan speed= 1740 1800 1800 3000	Temp= 27.0 27.0				info		1031400411AA	
COOL_UNIT1 :	Fan speed= 1860 1800 1800 3120	Temp= 30.0 29.0				info		1031400412AA	
AMC3 :	DAMC2	Deutsches Elektronen-Synchrotron	U= 3.3	Temp= 30.0		info	green	1065	
AMC2 :	X2TIMER	Stockholm University	U= 3.3	Temp= 29.0		info	green	0040	
RTM2 :	RTM_Trg1	Stockholm University				info	green	004	
RTM12 :	RTM-DWC	Struck Innovative Systeme GmbH				info	green	074	
RTM11 :	RTM-DWC	Struck Innovative Systeme GmbH				info	green	075	
RTM10 :	RTM-DWC	Struck Innovative Systeme GmbH				info	green	076	
RTM9 :	RTM-DWC	Struck Innovative Systeme GmbH				info	green	073	
RTM8 :	RTM-DWC	Struck Innovative Systeme GmbH				info	green	077	
RTM7 :	RTM-DWC	Struck Innovative Systeme GmbH				info	green	072	
MCH :	NAT-MCH v1.3, R130927	current= 2.2	Temp= 30.0	33.0	30.0	30.0	info		104
POWER_UNIT2 :	MTCA Power Sup...				Temp=	info		01886001	

## > Controls

- Real time capabilities
- DAQ
- Front-end (controls)
- Middle layer (Diagnostics)

## > Machine Protection

- Interlocks (MPS)
- Cryo OK ?
- LLRF alarm

## > RF Couplers

- QL control (motor / 3 stub tuners)
- Conditioning
- Interlocks (e-, light)
- Heating

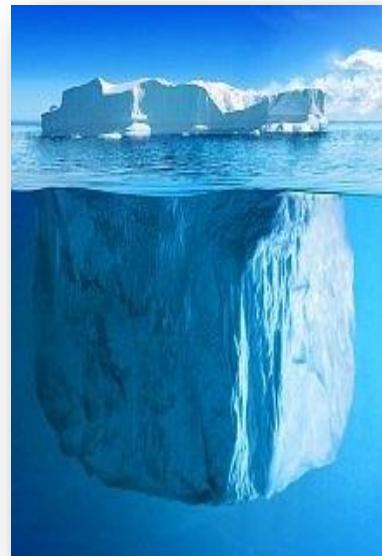
## > Personnel Protection

- Personnel interlock
- RF permit

## > Experiments

- RF Reference distribution
- Beam stability (BAM, energy)

## > ...

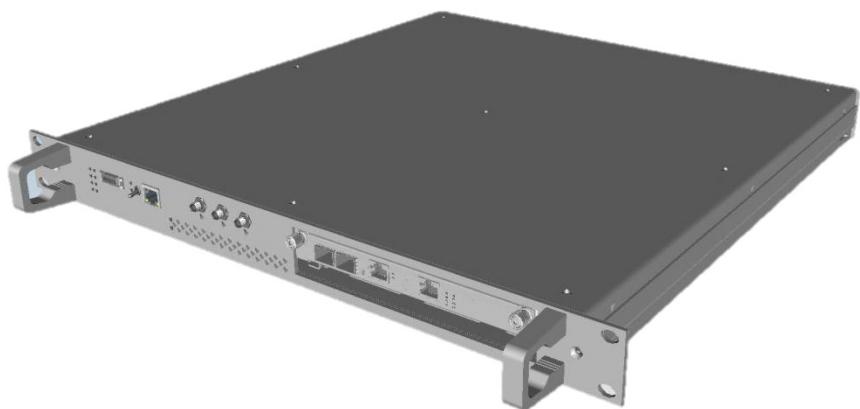


- > INTERFACES TO LLRF
- > LLRF FOR LARGE SCALE ACCELERATORS

# LLRF FOR LARGE SCALE ACCELERATORS

## > Mass production

- Specifications
- Call for tender
- Documentation
- 3D models
- Test procedure with firmware
- Non-conformity report
- Etc...



Deutsches Elektronen-Synchrotron  
Ein Forschungszentrum der Helmholtz-Gemeinschaft

Project/Project  
FLASH-WP02-xTCA

Title/Title Specification Document for the TMC-Board:

**TMCB**  
**(Temperature Monitoring & Control Board)**  
2.0 \*\*\* Draft Version \*\*\*

for all MTCA.4 components for FLASH, AMTF and XFEL

Autor/Author Marie Czwalina (MC)  
Mitautor(en) Jaroslaw Szewinski (JS), Jan Piekarzki (jP), Frank Ludwig (FL), Michael Fenner (MF), Borut Repš (BR), Gašper Jug (GJ)  
Co-Author(s)

External Reference/External Reference  
Board Revision Number 2.0

This document describes the requirements for the system subcomponent:  
Temperature Monitoring and Control Board. The module is a packaged prototype for the XFEL.

*Subcomponent : TMC-Board for all LLRF and synchronisation system 19" modules*

- Drift Calibration Module (DCM)  
- Local Axis Transient Monitor (BAT)  
- Laser-to-RF set-up (L2RF)  
- Local Oscillator Generation Module (LOGM)  
- Transverse Deflecting Structure (TDS)

approx. 50 pcs  
approx. 100 pcs  
approx. 15 pcs  
approx. 10 pcs  
approx. 10 pcs

Organization Verleihname Name of distribution Adresse Address Überprüfung (Name) Approved by name Datum Date

Document Status Status of document 1.8

Anzahl Seiten Number of pages 27

Datum Date 15.01.2014

FileServer  
FileName XFEI\_FLASH-WP02-xTCA\_TMCB\_Specs\_rev2\_15\_01\_2014\_MC\_FL\_MF\_JS\_Iteration\_8.docx

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XFEI\_FLASH-WP02-xTCA\_TMCB\_Specs\_rev2\_15\_01\_2014\_MC\_FL\_MF\_JS\_Iteration\_8.docx - 5 / 27

end on resistor configuration)  
1 MSPS, 4 –channels, (m x 9mm))

annels)  
ig (4x2), shielded  
0 - 2.5 V  
package TSSOP 16-leads)

ent connector type,  
uVpp (averaged over 2ms)  
ance input, unipolar range.

g (4x2), shielded  
istor programmable)

-4 LSB)  
50 Ohm

1 ft

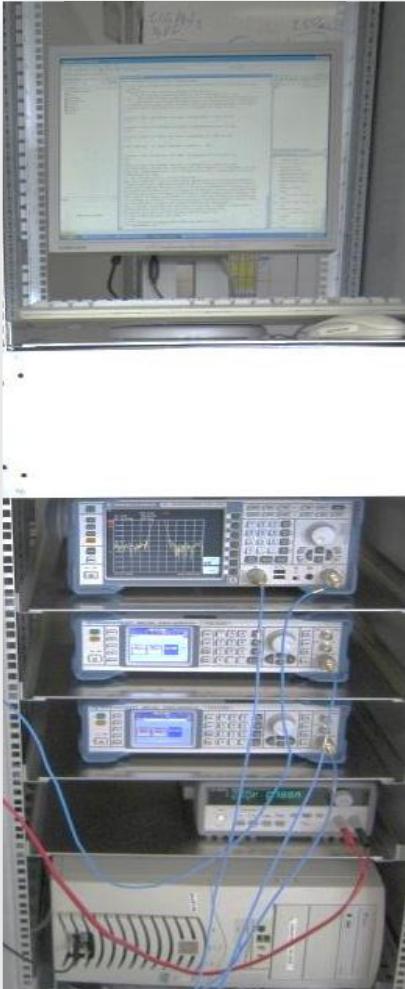
AD8876ARD  
1/2 AD8876

"(Updated version will follow.)



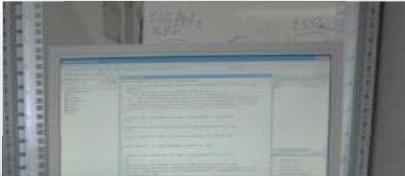
## > Quality Control

### Test Stands



## > Quality Control

### Test Stands



#### 6 ADC saturation level

ADC saturation levels at the plane of the connectors have been measured. ADCs have PASSED the tests.



Table 4: Channel saturation level at ERNI connector plane

#### 7 ADCs spectral purity

The spurious free dynamic range has been measured. ADCs have PASSED the tests.

Channel No.	SFDR	Status
1	-107.616523	OK
2	-106.659271	OK
3	-107.851373	OK
4	-106.617500	OK
5	-108.212449	OK
6	-108.907765	OK
7	-106.718074	OK
8	-109.275238	OK
9	-107.295380	OK
10	-101.138558	OK

Table 5: ADC 1st harmonic power measurement

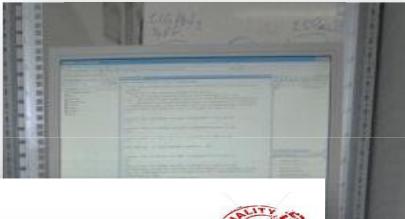
#### 8 Channel-to-channel crosstalks

Channel-to-channel crosstalks have been measured. DUT has PASSED the tests.



## > Quality Control

### Test Stands



#### 6 ADC saturation level

ADC saturation levels at the plane of the connectors have been tested.  
have PASSED the tests.

Channel No.	Saturation level [dBm]	Status
1	9.277498	OK
2	9.320781	OK
3	9.209005	OK
4	9.341573	OK
5	9.125885	OK
6	9.217273	OK
7	9.101135	OK
8	9.302086	OK
9	9.079008	OK
10	9.077752	OK

Table 4: Channel saturation level at ERNI connector plane

#### 7 ADCs spectral purity

The spurious free dynamic range has been measured. ADCs have PASSED the tests.

Channel No.	SFDR	Status
1	-107.616523	OK
2	-106.059271	OK
3	-107.851373	OK
4	-108.317389	OK
5	-108.254849	OK
6	-108.907765	OK
7	-106.718074	OK
8	-109.275238	OK
9	-107.295380	OK
10	-101.138558	OK

Table 5: ADC 1st harmonic power measurement

#### 8 Channel-to-channel crosstalks

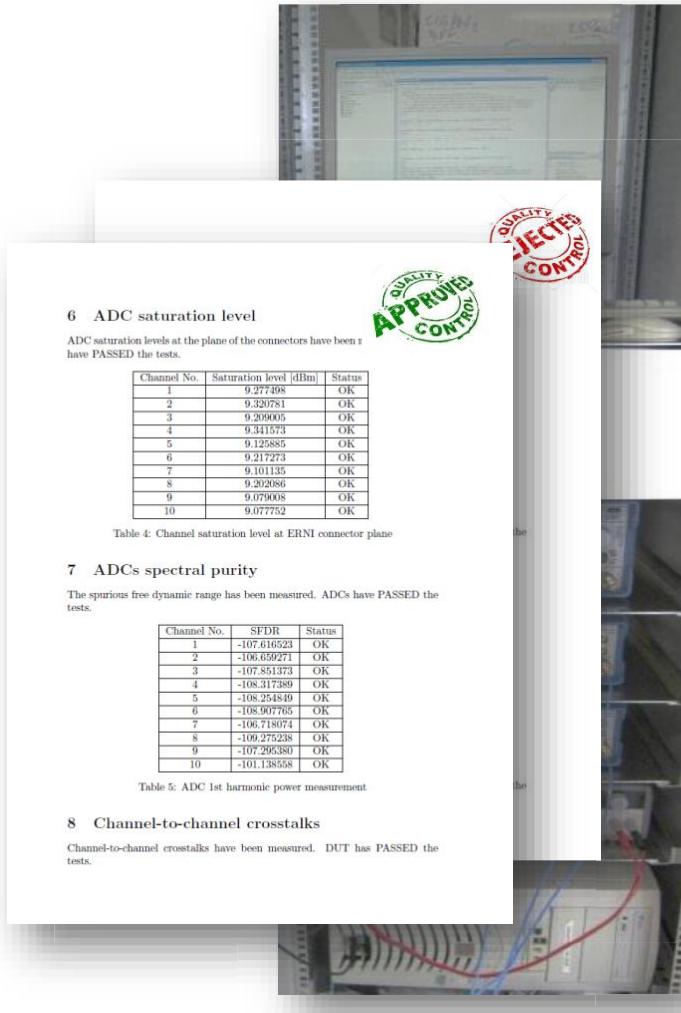
Channel-to-channel crosstalks have been measured. DUT has PASSED the tests.



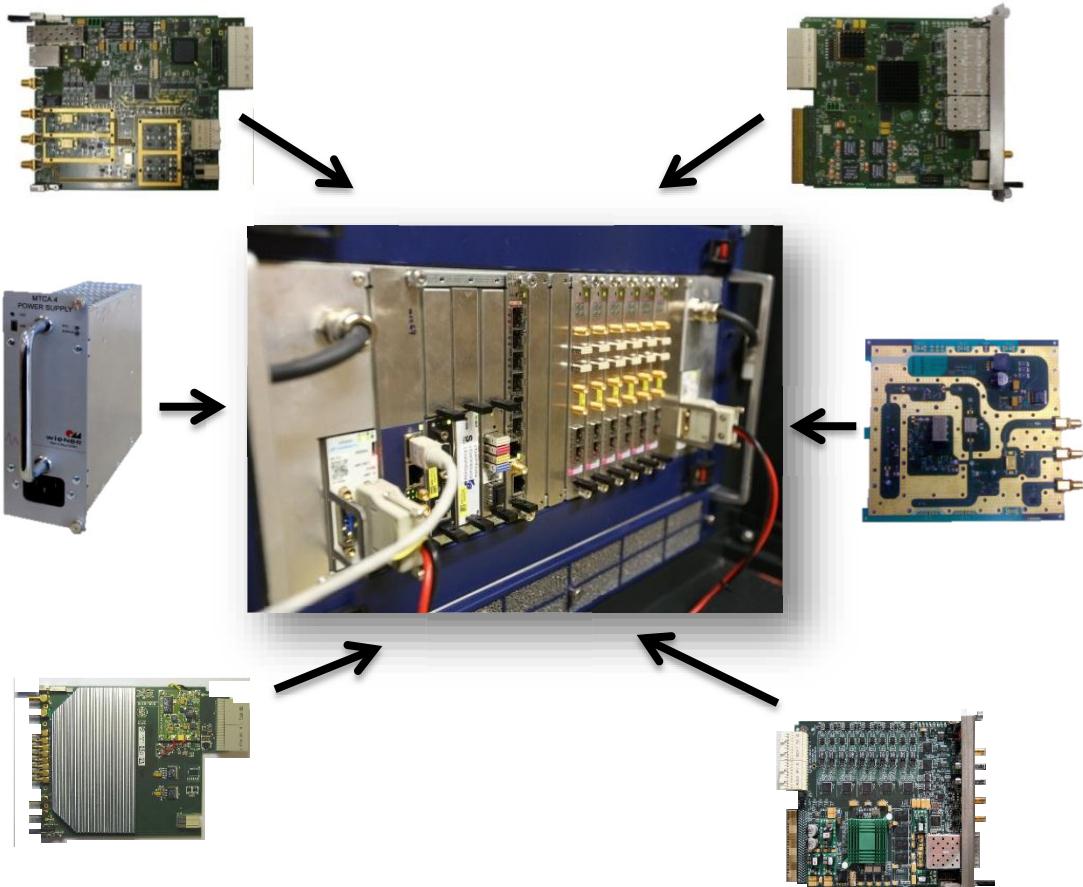
# LLRF FOR LARGE SCALE ACCELERATORS

## > Quality Control

### Test Stands



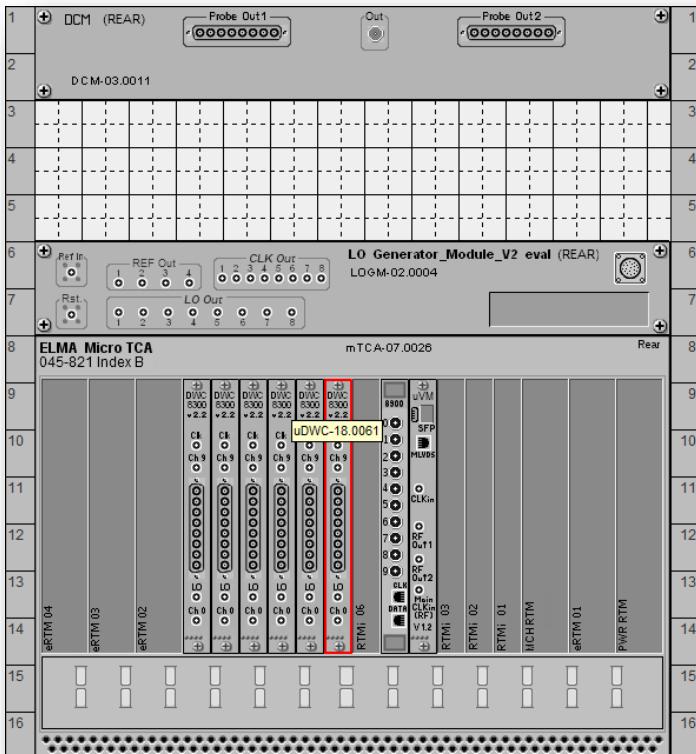
### System Integration



# LLRF FOR LARGE SCALE ACCELERATORS

## > Installation

- Procedure
- Check list
- Labelling
- Device tracking

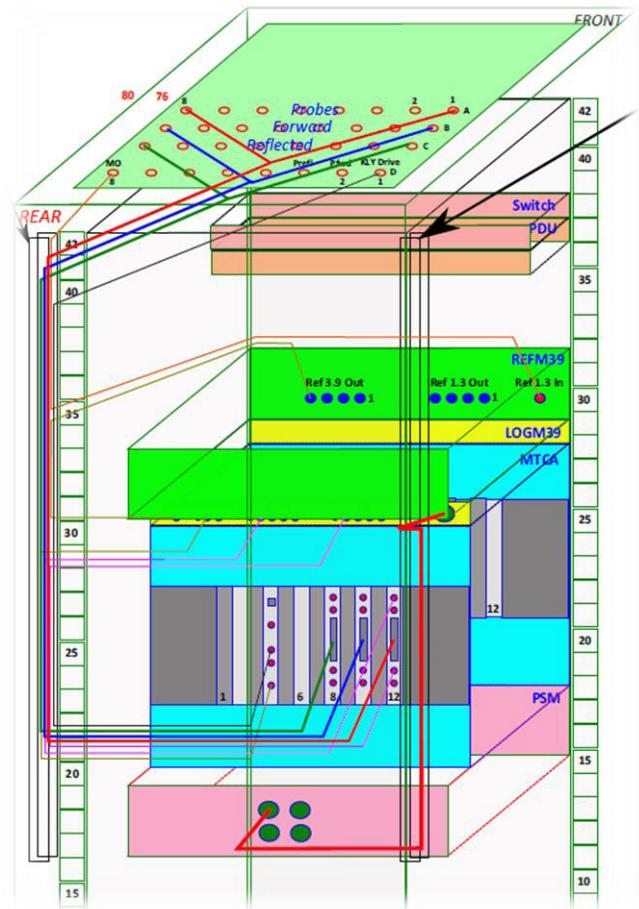
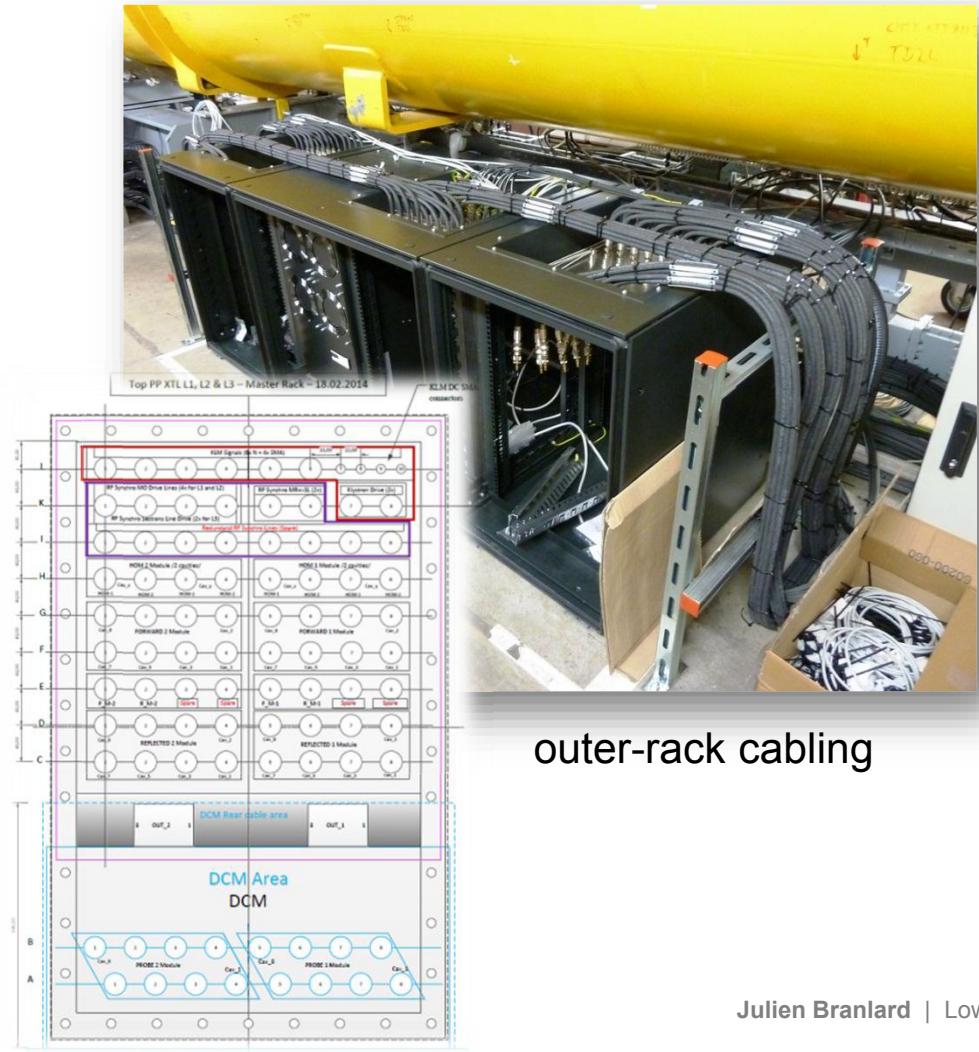


WP02 - LLRF						
 <b>Deutsches Elektronen-Synchrotron</b> Ein Forschungszentrum der Helmholtz-Gemeinschaft						
Title: <input type="text"/>						
WP02 LLRF						
MTCA crate installation check list						
Destination: <input type="checkbox"/> IN1 <input type="checkbox"/> L1 <input type="checkbox"/> L2 <input type="checkbox"/> L3 <input type="checkbox"/> RF station # <input type="checkbox"/> MASTER <input type="checkbox"/> SLAVE CPU name: <input type="text"/> MCH name: <input type="text"/>						
Shipment do <input type="checkbox"/> Labelling do <input type="checkbox"/> KDS entry do <input type="checkbox"/> Factory test: <input type="checkbox"/> Factory test: <input type="checkbox"/> Unit: <input type="checkbox"/> Resu: <input type="checkbox"/> Failure: <input type="checkbox"/> Ready-for-te: <input type="checkbox"/> Incoming ins: <input type="checkbox"/> by: <input type="checkbox"/>						
Slot	AMC	KDS #	Version	RTM	KDS #	Version
-1	uPM					
0	MCH					
1	CPU					
2	TMG					
3						
4	uTC			uVM		
5						
6						
7	uADC			uDWC		
8	uADC			uDWC		
9	uADC			uDWC		
10	uADC			uDWC		
11	uADC			uDWC		
12	uADC			uDWC		
13	uADC			uDWC		
14	uPM			uDWC		
15						
16						

Notes:

# LLRF FOR LARGE SCALE ACCELERATORS

## ➤ Large channel integration

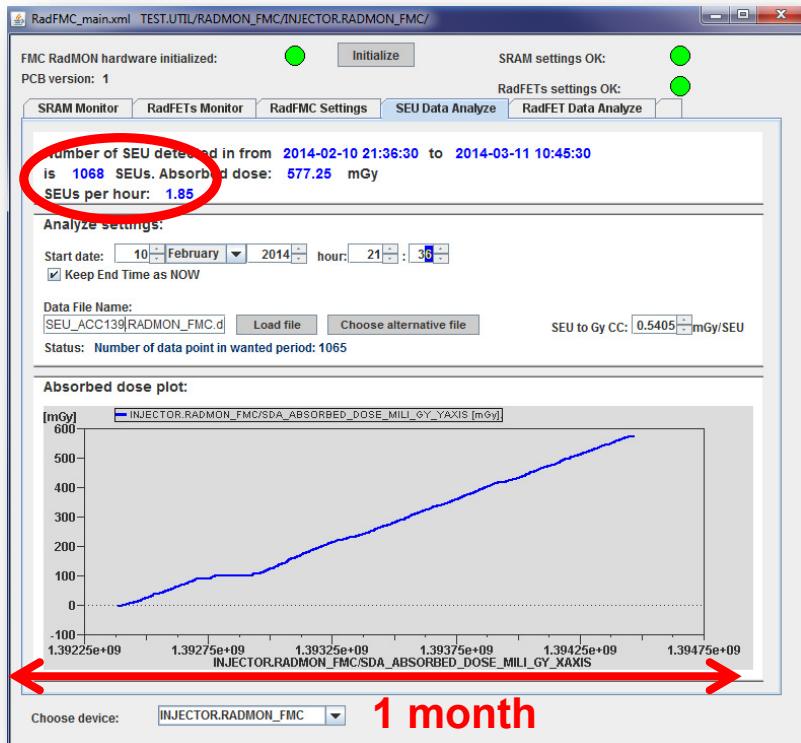


## inner-rack cabling

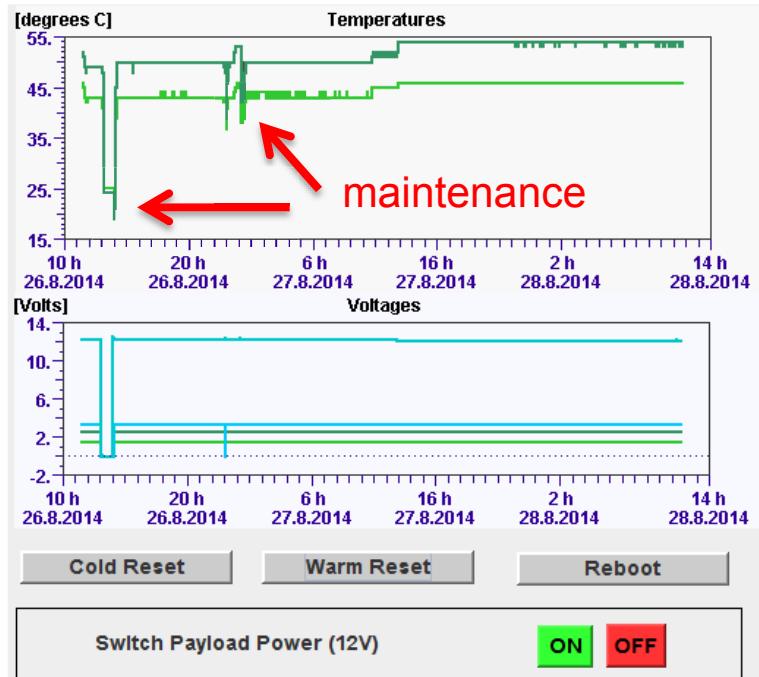
# LLRF FOR LARGE SCALE ACCELERATORS

## > Remote “everything”

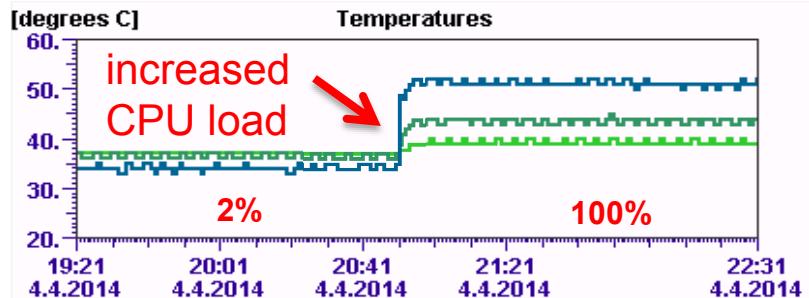
- System health monitoring
- System upgrades (FW / SW)
- Management (on / off / swap)



Radiation monitoring in tunnel



Temperature monitoring

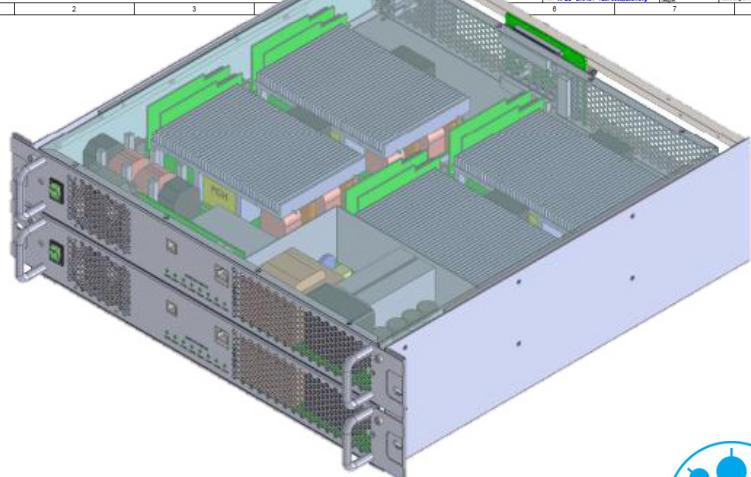
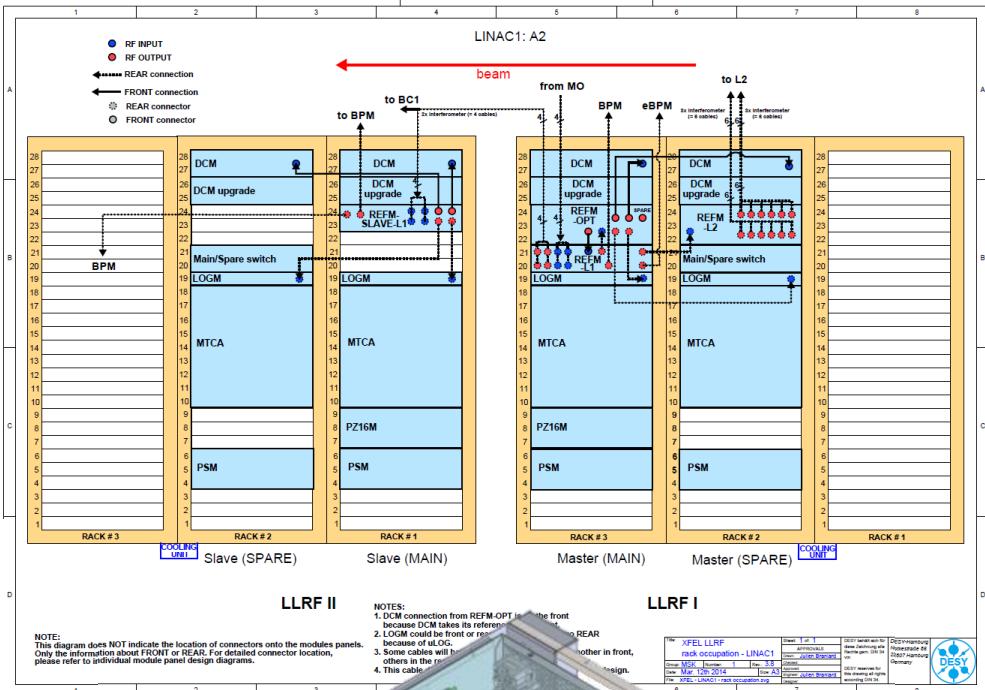


# LLRF FOR LARGE SCALE ACCELERATORS

## > Remote “everything”

- System health monitoring
- System upgrades (FW / SW)
- Management (on / off / swap)

## > Redundancy

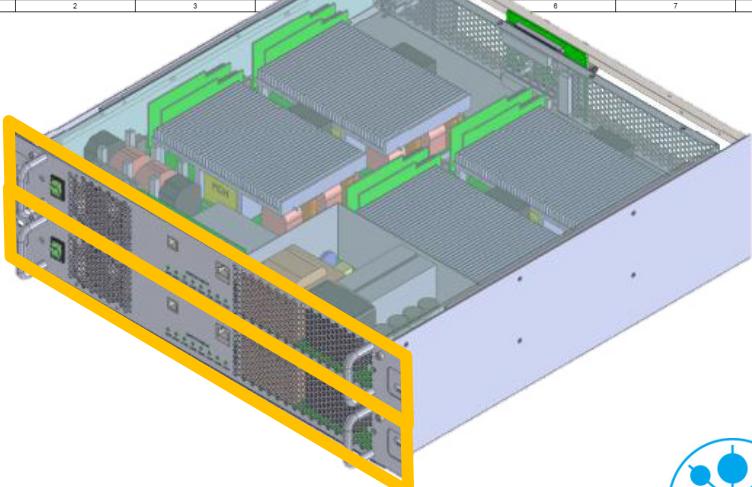
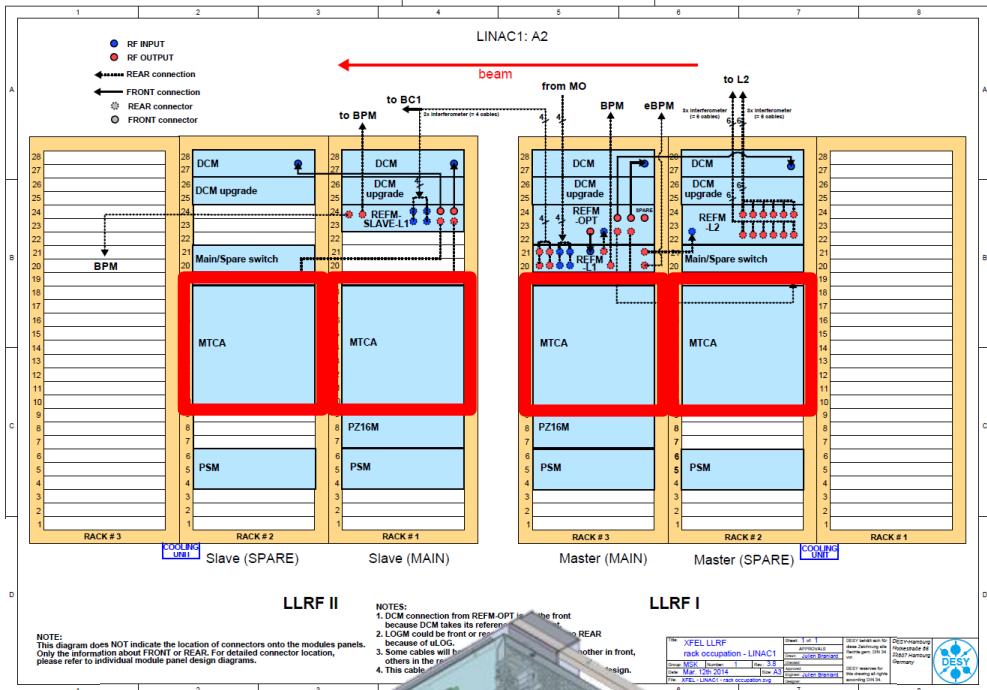
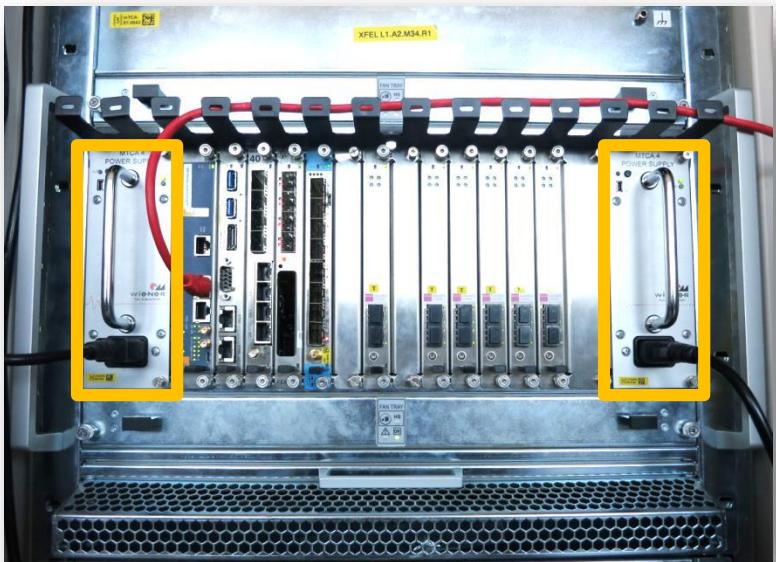


# LLRF FOR LARGE SCALE ACCELERATORS

## > Remote “everything”

- System health monitoring
- System upgrades (FW / SW)
- Management (on / off / swap)

## > Redundancy



## > Automation

- For operation
- For machine protection

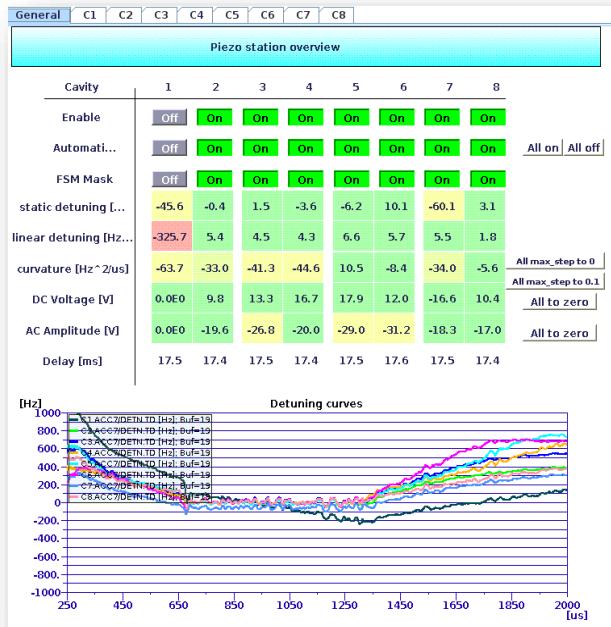
*Frequency tuning / detuning  
 Bandwidth control  
 Diagnostics  
 Quench  
 Startup/shutdown  
 Calibration  
 Performance*

...

### Cavity bandwidth control

C2.ACC7		C3.ACC7		C4.ACC7		C5.ACC7		C6.ACC7		C7.ACC7		C8.ACC7			
C3.ACC6		C4.ACC6		C5.ACC6		C6.ACC6		C7.ACC6		C8.ACC6		C1.ACC7			
Main - ACC6				Main - ACC7				C1.ACC6				C2.ACC6			
		C1	C2	C3	C4	C5	C6	C7	C8						
Move motor enable		<input type="checkbox"/> Enable													
ALL ON		<input type="button" value="STOP"/>													
ALL OFF		<input type="button" value="STOP"/>													
QL SP		000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000		
Avg QL		2452647	2477117	2770227	2636500	2956047	2973049	2860477	2888423						
QL error [%]		18.25	17.43	7.66	12.12	1.47	0.90	4.65	3.72						
Motor status		ready to be moved													
Motor pos. SP		57872	276662	81992	167624	242252	913724	309002	194507						
Motor current pos.		57872	276662	81992	167624	242252	913724	309002	194507						

### Cavity resonance control



# LLRF FOR LARGE SCALE ACCELERATORS

## > Automation

- For operation
- For machine protection

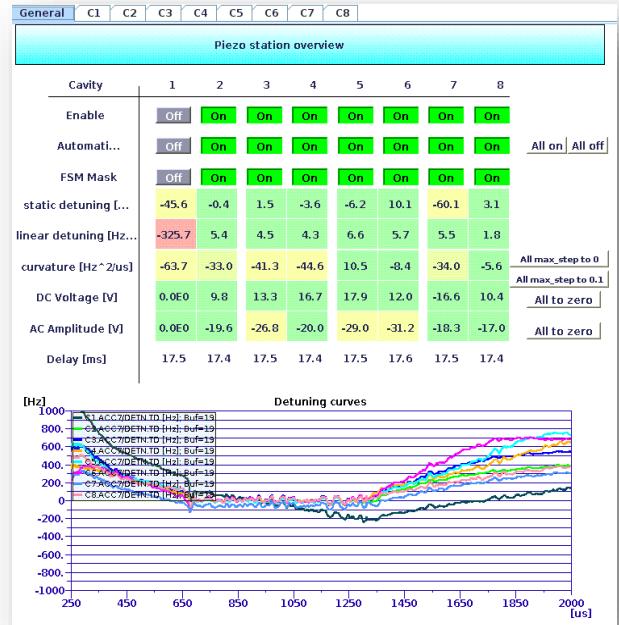
*Frequency tuning / detuning  
Bandwidth control  
Diagnostics  
Quench  
Startup/shutdown  
Calibration  
Performance  
...*

**Exception Handling  
Automation Priorities**

### Cavity bandwidth control

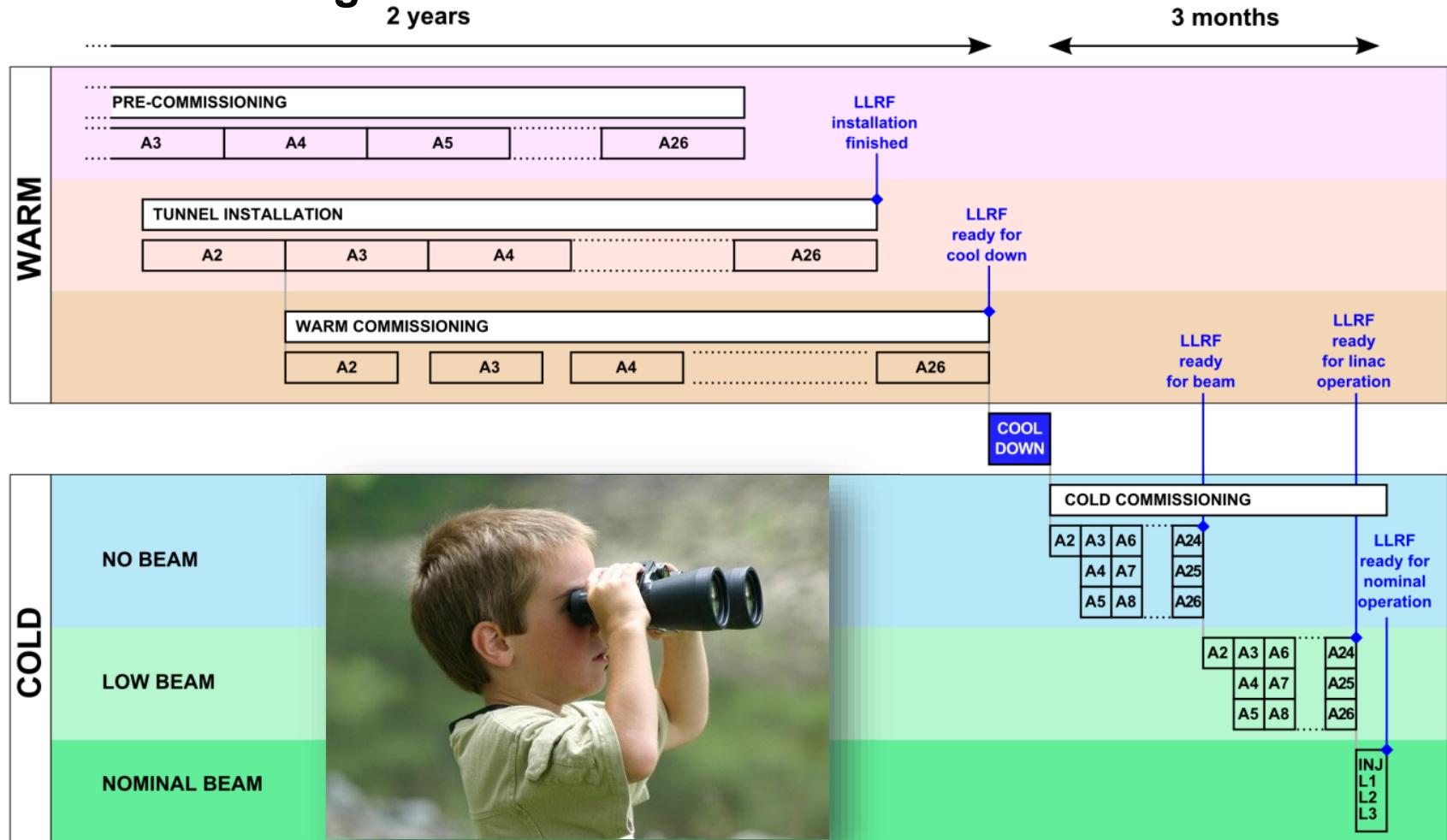
C2.ACC7		C3.ACC7		C4.ACC7		C5.ACC7		C6.ACC7		C7.ACC7		C8.ACC7			
C3.ACC6		C4.ACC6		C5.ACC6		C6.ACC6		C7.ACC6		C8.ACC6		C1.ACC7			
Main - ACC6				Main - ACC7				C1.ACC6				C2.ACC6			
		C1	C2	C3	C4	C5	C6	C7	C8						
<b>Move motor enable</b>		<input type="checkbox"/> Enable													
<b>ALL ON</b>		<input type="button" value="STOP"/>													
<b>ALL OFF</b>		<input type="button" value="STOP"/>													
<b>QL SP</b>		000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000		
<b>Avg QL</b>		2452647	2477117	2770227	2636500	2956047	2973049	2860477	2888423						
<b>QL error [%]</b>		18.25	17.43	7.66	12.12	1.47	0.90	4.65	3.72						
<b>Motor status</b>		ready to be moved													
<b>Motor pos. SP</b>		57872	276662	81992	167624	242252	913724	309002	194507						
<b>Motor current pos.</b>		57872	276662	81992	167624	242252	913724	309002	194507						

### Cavity resonance control



# LLRF FOR LARGE SCALE ACCELERATORS

## > Commissioning



# QUESTIONS?

